# **SUMMIT'S**

# RCA integrated integrated circuits (\*\*) Product guide



SUMMIT DISTRIBUTORS, INC.
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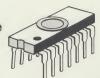
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Ceramic flat pack



Ceramic dual in-line



Plastic dual in-line



Instrume	• Teleme												_
		C	702C			30 CA	35V1 3036 CA3037 CA3037A CA3038		A3	039 A3041 CA3042 CA3043 CA3044		3044V1 CA3045 CA3046	Application
													DC Amplifier
													Audio Amplifier
													Video Amplifier
									П				IF Amplifier
													RF Amplifier
													Analog Switch
													Comparator
											-		Detector: AM, FM, Phase, Product
													Differential Amplifier
													Limiter
						T							Mixer: AF, RF
7						$\top$							Modulator
						$\dagger$							Multivibrator
													Operational Amplifier
						T							Oscillator
						+							Schmitt Trigger
						+					_		Sense Amplifier
													Switching: Analog, Power
													Features
				$\Box$					П				Balanced Input
						T					$\overline{}$		Balanced Output
									П				Low Noise (1/f)
								-			-		Multiple Simultaneous Functions
													Internal Regulated Power Supply
						1							Class B Power Output
						1							AGC Capability
			-		-								Special-Function Sub-System
												-	Transistor or Diode Array
													Package
N													Ceramic Flat Pack
													Ceramic Dual In-Line
													Plastic Dual In-Line
													"TO-5" Style



# **Absolute Maximum Ratings**

II Inca	ilatea					1
circu	its <b>©</b>	Ambient Temp. R		Input Sig. Vo	lt.	Maximum DC Input Power
RCA Type	Applications and Features  For Circuits, See Page 23	Oper- ating °C	Stor- age °C	Single Ended V	Common Mode V	P <sub>T</sub> mW
Differen	tial Amplifiers					
CA3000	DC Amplifier  •Schmitt Trigger •RC Coupled Feedback Amplifier •Mixer •Comparator •Crystal Oscillator •Sense Amplifier •Modulator •Wide AGC Range: 90dB typ.	-55 to +125	-65 to +200	±2	±2	300
CA3001	DC, IF, and Video Amplifier  •Schmitt Trigger •Mixer •Modulator •Differential Input and Output  •Emitter-Follower Input and Output •—3dB Bandwidth: 29MHz	_ ·		±2.5	±2.5	
CA3002	IF and Video Amplifier  ◆Product Detector ◆AM Detector ◆Schmitt Trigger ◆—3dB Bandwidth: 11MHz		Application of the second of t	±3.5		
CA3004	RF/IF Amplifiers  • Push-Pull Input and Output •AGC • Detector • Wide- and Narrow-Band Amplifier • Mixer • Limiter • Modulator • Cascode Amplifier • Video Amplifier			±3.5	+3.5 to -2.5	
CA3005	<ul> <li>CA3004 has linear transfer characteristics, excellent circuit stability and wide dynamic range</li> <li>CA3005 and CA3006 feature high gain, sharp limiting characteristics, and exceptional versatility</li> <li>CA3005 and CA3006 are identical except for input offset voltage:</li> </ul>		management of the fig. (i.e., i.e.,	±3.5		
CA3006	CA3006, 1 mV. max. CA3005, 2.6 mV. typ.	_		±3.5	+3.5 to -2.5	
CA3007	● AF Amplifier  • Audio Driver • Audio Amplifier • Sound Systems and Communications  Equipment • Eliminates Need for Audio Driver Transformer		VVA na destinativo como A.A., prosepando.	±2.5	±2.5	
CA3020	AF Amplifier Applications  • Combines Functions of Preamplifier, Phase Inverter, Driver, and Push-Pull					r the following:
CA3020A	Output on Single Pellet •Audio Preamplifier •Power Amplifier •Servo Amplifier •Power Gain: 58dB typ. •—3dB Bandwidth: 6MHz typ. •Two Input Levels: 40kΩ and 1000Ω •Squelch Flexibility: 3 methods for applying squelch		vity— CA30	20 (400n	ifier— CA3020 CA3020 nW)=35mV typ nwW)=50mV ty	
CA3026	Ratings and Characteristics are shown under Transistor Arrays					<u> </u>
CA3028A	RF/IF Amplifier  • Differential or Cascode • Converter for FM Broadcast Band • Limiter • Mixer  • Oscillator • Useful Frequency Range DC to 120MHz • Only One Power Supply Required • Audio, Sense, and DC Amplifier • Low Substrate and Feedback Capacitance for high bandwidth capability	-55 to +125	-65 to +200		peak peak	For $T_A \le 85$ °C $P_T = 450$ mW For $T_A > 85$ °C derate 5 mW/°C
CA3028B	CA3028B has all the features of CA3028A but in addition is controlled for Input-Offset Voltage, Input-Offset Current, Input Bias Current and features Single- and Dual-ended Operation			4 @ V.	to $-2.5$ = +6V = -6V	For $T_A \leq 85$ °C $P_{\tau} = 450$ mW
	The CA3028B is especially suitable as a DC and Differential Amplifier in Control Circuits  Data for dual-ended operation; for single-ended operation, refer to CA3028A		Video marine de de deservación de marine de deservación de deserva	7	to $-5$ $c = +12V$ $c = -12V$	For T <sub>A</sub> >85°C derate 5mW/°C
High-Ga	in Wide-Band Amplifiers		1			
CA3011	<ul> <li>FM IF Amplifiers •Wide-band Amplifiers •High Amplifier Gain: 75dB typ. at 4.5MHz •Limiting Sensitivity: 600 μV typ at 10.7MHz (at knee)</li> <li>Useful Frequency Range: 100kHz to &gt;20MHz</li> <li>CA3011 is like the CA3012 except for max. recommended dc supply voltage: CA3011, 7.5V; CA3012, 10V</li> </ul>	-55 to +125	-65 to +200	±3	_	300
CA3012	•Input Limiting Voltage (at knee) = 300 µV at f=4.5MHz	<b>—55</b>	-65	±3	The second secon	300
		to +125	to +200		or not obtained the second of	
CA3013	FM IF Amplifier/Limiter/FM Detector/AF Amplifiers  • Comprehensive circuit functions in a single package: IF Amplifier, AM and Noise Limiter, FM Detector, Audio Preamplifier  • Excellent AM Rejection: > 50dB at 4.5MHz	• Recover CA3013 i	ed AF Volta	ge: 220m 14 except	p. at 4.5MHz V typ. at 4.5MH for max. recom	z, 25kHz deviation mended dc supply voltage:
CA3020	AF Amplifier Applications (Combines Functions of Preamplifier, Phase Inverter, Driver, and Push-Pull Output on Single Pellet)  •Audio Preamplifier •Power Amplifier •Useful Freq. Range: DC to 8MHz with resistive load; into VHF with Tuned Circuits •Servo Amplifier  •Power Gain 58db typ. •—3dB Bandwidth: 6MHz typ. •Two Input Levels: 40KΩ and 600Ω •Squelch Flexibility: 3 methods for applying squelch	-55 to +125	-65 to	±3	For $T_A \le 25^\circ$ $P_T = 1W$ For $T_A > 25^\circ$ derate 6.7m With Heat S	C W/°C
CA3020A	The CA3020A is like the CA3020 except for the following:  Power Output Class B Amplifier— Sensitivity—  CA3020 ( $V_{cc}$ =9V)=0.55mW typ. CA3020 (400mW)=35mV typ.  CA3020A ( $V_{cc}$ =12V)=1mW typ. CA3020A (800mW)=50mV typ.				For $T_c \le 55^\circ$ $P_T = 2W$ For $T_c > 55^\circ$ derate 16mV	C O

Typical	Electrical	Characteristics	at T. = 25°C
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	-				-	11C	Dynam	7		-:			Static		
			nd Output ince	Input a Impeda			Noise Figure		Gain●	Device	DC				DC Si Voltag
RCA Type	Pkgs See Page 3	Output Ω	Input Ω	Test Freq. MHz	CMR Ratio f=1kHz dB	NF dB	Test Freq. MHz	Useful Frequency Range MHz	Gain dB	Test Freq. MHz	Input Power Drain mW	Input Bias Current µA	Offset Voltage mV	V <sub>cc</sub>	V <sub>EE</sub> V
	-	Differe	Г					\$	1	+		1		-1	
CA3000	TO-5	8k	195k 2% typ. at	1kHz tion=0.	98 ic Distor	— Harmon	— Total	DC to 30	32a 37b	1kHz	30	23	1.4	+6	-6
CA3001	TO-5	45	140kf 3.4pFf	1.75	88	5 7.7	1.75 11.7	DC to 100	19 14	1.75	78	16	1.5	+6	-6
CA3002	TO-5	70	100kf 4pFf	1.75	_	4	1.75	DC to	24 diff.	1.75	55	20	2.2 unbal.	+6	-6
CA3004	TO-5	2.2k	1.2k	100	98	6.3	100	DC to 120	12e	100	26	21	1.7	+6	<b>-6</b>
CA3005	TO-5	2k	1.4k	100	101	7.8c 7.8d	100	DC to 120	20c,e 16d,e	100	26	19	2.6	+6	-6
CA3006	TO-5	2k	1.4k	100	101	7.8c 7.8d	100	DC to 120	20c,e 16d,e	100	26	19	0.8	+6	-6
CA3007	TO-5	60 at f=1kHz		1kHz tion=0.	77 ic Distor	— Harmon	— Total	DC to 20kHz	22e	1kHz	30	11	0.57 unbal.	+6	-6
CA3020	TO-5			iers	Amplif	e-Band	n Wide	r High-Gai	n unde	show	ics are	cteristi	d Chara	ngs an	Rati
CA3020	TO-5														
CA3026	TO-5						rays	ransistor A	under T	hown	s are s	teristic	Charac	gs and	Ratir
CA3028	TO-5	_		1			1	DC4-	20c,e	100	1	16.6d		+6	0
	1000000	12.5kc	590c	10.7 10.7	to transition of the second sec	7.2c		DC to 120	17d,e 40c	10.7 10.7 10.7	White the second		_	+6 +9 +9	0
CA3028	TO-5	2950d —	1400d	10.7 1kHz	discontinuos de la contra del la contra de la contra del la con	6.7d t—3dB MHz	100 100 BW a =7.3		17d,e 40c 30d		_	16.6d	0.98	+6 +9 +6	0
CA3028	TO-5	2950d — t f=1kHz —	1400d 5.5k 1.5V <sub>p-p</sub> at	10.7 1kHz wing=1 1kHz	oltage Sv	6.7d t—3dB MHz Output Vo at—3dB IHz	100 100 BW a = 7.3 Max C BW a = 8M	DC to 120	17d,e 40c 30d 38 2kΩ	10.7 10.7 1kHz at R=	36	16.6d	_	+9 +9 +6	0 0 6
		2950d	1400d 5.5k 1.5V <sub>p-p</sub> at 3k 3V <sub>p-p</sub> at f	10.7 1kHz wing=1 1kHz wing=2	90 oltage Sv	6.7d t—3dB MHz Output Vo at—3dB IHz	100 100 BW a = 7.3 Max C BW a = 8M	DC to 120	17d,e 40c 30d 38 2kΩ	10.7 10.7 1kHz at R=	36	16.6d	0.98	+9 +9 +6	0 0 6
		2950d — t f=1kHz —	1400d 5.5k 1.5V <sub>p-p</sub> at 3k 3V <sub>p-p</sub> at f	10.7 1kHz wing=1 1kHz wing=2	90 oltage Sv	6.7d t—3dB MHz Output Vo at—3dB IHz	100 100 100 BW a = 7.3 Max C BW a = 8M Max C	DC to 120	17d,e 40c 30d 38 2kΩ	10.7 10.7 1kHz at R=	36	16.6d	0.98	+9 +9 +6	0 0 6
nplifier	and A	2950d	1400d 5.5k 1.5V <sub>p-p</sub> at f 3k 3V <sub>p-p</sub> at f Gain W	10.7 1kHz wing=1 1kHz wing=2	90 oltage Sv	6.7d t—3dB MHz Output Vo at—3dB IHz Output Vo		DC to 120 DC to 120	17d,e 40c 30d 38 2kΩ 42.5 =1.6kΩ	10.7 10.7 1kHz at R = 1kHz at R = 14.5 10.7	36	16.6d	0.98	+9 +9 +6 +12	0 0 -6 -12
nplifier CA3011	and A	2950d	1400d 5.5k  1.5V <sub>p-p</sub> at f  3k  3V <sub>p-p</sub> at f  3kf 7pFf  3kf	10.7 1kHz wing=1 1kHz wing=2 igh-0	90 Ditage Sv	6.7d t—3dB MHz Output Vo It—3dB IHz Output Vo 8.7	BW a = 7.3 Max CBW a = 8M Max CB	DC to 120 DC to 120 100kHz to >20MHz 100kHz to >20MHz	17d,e 40c 30d 38 2kΩ 42.5 =1.6kΩ 70 67 61 70 67 61	10.7 10.7 1kHz at R = 1kHz at R = 14.5 10.7	175 120		0.98	+9 +9 +6 +12 +7.5	0 0 -6 -12
nplifier CA3011 CA3012	TO-5	2950d	1400d 5.5k  1.5V <sub>p-p</sub> at f  3k  3V <sub>p-p</sub> at f  3kf 7pFf  3kf	10.7 1kHz wing=1 1kHz wing=2 igh-0	90 Ditage Sv	6.7d t—3dB MHz Output Vo It—3dB IHz Output Vo 8.7	BW a = 7.3 Max CBW a = 8M Max CB	DC to 120 DC to 120 100kHz to >20MHz 100kHz to	17d,e 40c 30d 38 2kΩ 42.5 =1.6kΩ 70 67 61 70 67 61	10.7 10.7 1kHz at R = 1kHz at R = 14.5 10.7	175 120		0.98	+9 +9 +6 +12 +7.5	0 0 -6 -12
nplifier CA3011 CA3012	TO-5	2950d	1400d 5.5k  1.5V <sub>p-p</sub> at f  3k  3V <sub>p-p</sub> at f  3kf 7pFf  3kf	10.7 1kHz wing=1 1kHz wing=2 igh-0	90 90 H	6.7d t—3dB MHz Output Vo It—3dB IHz Output Vo 8.7		DC to 120 DC to 120 100kHz to >20MHz 100kHz to >20MHz	17d,e 40c 30d 38 2kΩ 42.5 =1.6kΩ 70 67 61 70 67 61	10.7 10.7 1kHz at R = 1kHz at R = 14.5 10.7 14.5 10.7	175 120 120	16.6d  36d  cteristic		+9 +9 +6 +12 +7.5	0 0 -6 -12

Voltage gain unless specified
cuit e Gp=Power Gain
circuit f Parallel resistance and capacitance

a single-ended output b double-ended output

c cascode circuit d differential circuit

g  $V_{cc} = +9V$ 

		Static						Dyna	mic						
DC Supp Volta		- Input	Input	DC Input	Device Voltag gain u specifi	e nless	Useful	Noise Figur		Common Mode Rejec- tion	Input	and Outp	out	Device	
V <sub>EE</sub>	Vcc	Offset Voltage mV	Bias	Power		Gain dB	Frequency Range MHz	Test Freq. MHz		Ratio f=1kHz dB	Test Freq. MHz	Input $\Omega$	Output $\Omega$	Pkgs See Page 3	RCA Type
										Н	iah-(	Gain V	Vide-Ba	and A	mplifier
0	+6			4	0.5	56	DC to	1	4.2	_	1	4kf 11pFf	300 Resistive	TO-5	CA3021
0	+6	_		12.5		57	DC to	1	4.4	_	5	1.3kf 18pFf	120 Resistive		CA3022
0	+6			35	5 10	53 44	DC to	1	6.5	_	10	300f 13pFf	100 Resistive	TO-5	CA3023
Rat	ings an	d Chara	cteristic	s are	showr	unde	Special-Fu	inctio	n Sub	systems		•		TO-5	CA3035
 D .:		1.01				0.4.0.0	0514								
 Katı	ngs and	l Charac	teristic	sıden	tical to	CA30	)35V1							TO-5	CA3035V
Rati	ngs and	d Charac	cteristic	s are s	hown	under	Special-Fu	nction	Subs	vstems				DIP	CA3041
			,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	0 0.00		undor	opoolal 1 a	1101101	, oub	,,0101110				TO-5	CA3043
 											Sn	ecial-f	unction	s Sub	svstems
0	+7.5	The state of the s		120	1 4.5 10.7	70 67 60	100kHz to >20MHz	4.5	8.7	-	4.5	3kf 7pFf		TO-5	CA3013
0	+7.5			120	1 4.5 10.7	70 67 60	100kHz to >20MHz	4.5	8.7	_	4.5	3kf 7pFf	31.5kf 4.2pFf	TO-5	CA3014
_	+10	Referen Quiesce	ng Curre ce Volta	nt into ge at T ating V	nA typ. Termin ermina	al No. 2 I No. 9:	<del></del>	-	Voltag Termin	t Offset ge Between nals No. 4 : 0V typ.	1	2k	-	TO-5	CA3034
 Rati	ngs and	-			tical to	CA30	034 but has	forme	d lead	S.				TO-5	CA3034V1
-	+9	5mA ty	Prain:	#1			-3dbB-W 500kHz	1kHz	6		ut. Vol	z 50k t Swing:	$=2V_{p-p}$	TO-5	CA3035
		Sens. 1	=+13V	1++2			2.5		_			Swing	170 = 2.6V <sub>p-p</sub>		
		Relayr	IIA-7.0	Ampl #3	.40kH:	z 42	2.5	_		Typ. O	40kH: ut. Voli	5 670 5 Swing	100k =8V <sub>p-p</sub>	######################################	
 	ngs and	Charac					35 but has f							TO-5	CA3035V1
to Te	140V erminal		_	250	4.5 AF Dr	67	0.1 to 20	Input	eatf= R <sub>IN</sub> =	er Resis- = 1kHz: =100kΩ	4.5	11kf 5pFf		14-lead DIP	CA3041
	ru s res. S.2kΩ	Operation was a second control of the control of th		desamente in convenience de desamente de des	1kHz			Outp	ut— n <sub>oi</sub>	<sub>στ</sub> =30kΩ					
Tota	I Harmo	onic Dis	tortion				.5MHz; V <sub>оит</sub>					= ± 25kF			
				240	AF Dr 1kHz		AF Driver Input—R <sub>IN</sub> : Output—R <sub>o</sub>	=100k	Ω	atf=1kH:	z:			DIP	CA3042
V <sub>cc</sub> = thru s resiste R <sub>L</sub> =7	or	Curren 6V into Pin No I <sub>11</sub> =16	.11,	225	10.7	80	100kHz to >20MHz	Disto f=10 V <sub>IN</sub> = Reco	Harmo rtion = .7MHz 1mV vered A ge = 10	1% at	10.7	7kf 5pFf	Input Limiting Voltage at —3dB =50µV	TO-5	CA3043
V <sub>cc</sub> = thru s resist R=1.	or	Curren 9V into Pin No I <sub>10</sub> =4n	.10,	140	Input A Forwar	Admittard Trans	Voltage (kne nce=0.5+j1. fer Admittanc tance = 0.077	e) = 7 1 mmhc e = -	5mV 11.7+		at	values 45.75M		TO-5	CA3044
Ratir	ngs and			Ident	ical to	CA30	44 but has f	ormed	leads	S.				TO-5	CA3044V1



Absolute Max. Ratings

circu			Device Dis	sipation	Ambicat	T	Max. Ratir	igs Each Tra	nsistor
				I	Ambient Range	remp.	Collector N	la=50; Diod	eV=-3to+12
RCA Type	Applications and Feature	For Circuits, See Page25	Any Transistor or Diode P mW	Total Package P mW	Oper- ating °C	Stor- age	Collector to Emitter Voltage V <sub>CEO</sub> V	Collector to Base Voltage V <sub>CBO</sub> V	Emitte to Ba Voltas VEBO V
Diode	Arrays					-			
CA3019	Six Diodes on a Single Silicon Chip. Four in "Quad" or Bridge Configuration, and Two Isolated Diodes.  •Matched Diodes •Low Leakage		20	120	-55 to +125	-65 to +200	* 1000000000000000000000000000000000000		_
CA3039	Six Independent Diodes on a Single Silicon Chip Connected as Two Differential Amplifiers with Constant Current Sinks		20	450	-55 to +125	-65 to +200		The second secon	_
Transis	stor Arrays						-		
CA3018	Four NPN Transistors on Single Silicon Chip. Two are Isolated with no Interconnections. Other Two Have Emitter-to-Base Interconnection for	Use as a Darlington Pair, a Common- Emitter Amplifier with a Decoupled Common-Base Amplifier, an Amplifier with Protective Diode at Input, or Two Diodes in Series Back-to-Back.	300	300	-55 to +125	-65 to +200	15	20	4
CA3018A	The CA3018A is similar to the CA3018 but features tighter control of current gain, leakage and offset	parameters, making it suitable for applications requiring premium performance.	300	300	−55 to +125	-65 to +200	15	20	5
CA3026	Six Transistors on a Single Silicon Chip Connected as Two Differential Amplifiers with Constant Current Sinks		.300	450	-55 to +125	-65 to +200	15	20	5
CA3036	Two Independent Low-Noise Wide-Band Amplifier Channels     Particularly Useful for Preamplifier and Low-Level Amplifier Applications in Single-Channel and Stereo Systems	Wide Application in Low-Noise Instrumentation Amplifiers     Matched Transistors With Emitter-Follower Outputs     200-MHz Gain-Bandwidth Product	300	300	—55 to +125	-65 to +200	_	_	_
CA3045	Three Isolated Transistors and One Differentially-Connected Transistor Pair	• For all Types of Signal-Processing Systems operating from DC to VHF • Temperature Compensated Amplifiers	300	750	-55 to +125	-65 to +200	15	20	5
CA3046	The CA3046 is electrically like the CA3045 but is in a dual in-line plastic package for use in applications requiring a limited temperature range.		300	750	0 to +85	-25 to +85	15	20	5

## **Amplifier Arrays**

CA3035 Ratings and Characteristics are shown under Special-Function Subsystems

CA3035V1 Identical to CA3035 but has formed leads.

# Technical Data Booklets, Associated Application Notes

A Sample copy of a Technical Data Booklet and its Associated Application Note, shown below, is available on request from Commercial Engineering, RCA Electronic Components, Bldg. GM-1, Harrison, N. J. 07029.

RCA ype	Associated Application Note No.	File No.	Description	RCA Type	Associated Application Note No.	File No.	Description
inear Int	egrated Circuit	S					
A3000	ICAN-5030	121	DC Amplifier	CA3021)		243	Video and Wide-Band Amplifier
A3001	ICAN-5038	122	Video and Wide-Band Amplifier	CA3022 }	ICAN-5338	243	Video and Wide-Band Amplifier
A3002	ICAN-5036	123	IF Amplifier	CA3023 \		243	Video and Wide-Band Amplifier
A3004	ICAN-5022	124	RF Amplifier	CA3026			Dual Differential/Cascode Amplifier
A3005	(ICAN-5022)	125	RF Amplifier	CA3028A (	ICAN-5337	327	Differential/Cascode Amplifier
A3006	{ICAN-5269}	125	RF Amplifier	CA3028B		327	Differential/Cascode Amplifier
A3007	ICAN-5037	126	AF Amplifier	CA3029	(ICAN-5015)	316	Operational Amplifier
A3008		316	Operational Amplifier	CA3029A (	ICAN-5213	310	Operational Amplifier
A3008A	(ICAN-5015)	310	Operational Amplifier	CA3030 (	ICAN-5290	316	Operational Amplifier
A3010 (	(ICAN-5290)	316	Operational Amplifier	CA3030A	(10,111 0200)	310	Operational Amplifier
A3010A		310	Operational Amplifier	CA3031/702A {	ICAN-5290	241	Operational Amplifier
43011		128	FM IF Amplifier	CA3032/702C	10/111 0200	241	Operational Amplifier
A3012 (	(ICAN-5269)	128	FM IF Amplifier	CA3033 }	ICAN-5641	317	Operational Amplifier
43013	(ICAN-5380)	129	FM IF Amplifier/Discriminator/AF Amplifier	CA3033A	10/11/00/1	317	Operational Amplifier
A3014		129	FM IF Amplifier/Discriminator/AF Amplifier	CA3034		273	High-Freq. Wide-Band Amp/Phase Detector
A3015	(ICAN-5015)	316	Operational Amplifier	CA3034V1	(	273	High-Freq. Wide-Band Amp/Phase Detector
A3015A	ICAN-5213	310	Operational Amplifier	CA3035	{ST-3470}	274	Ultra-High-Gain Wide-Band Amp. Array
A3016	/ICAN-5290	316	Operational Amplifier	CA3035V1		274	Ultra-High-Gain Wide-Band Amp. Array
A3016A	,	310	Operational Amplifier	CA3036		275	Dual Darlington Array
A3018	(ICAN-5296)	338	Transistor Array	CA3037	(ICAN-5015)	316	Operational Amplifier
43018A	{ICAN-5290}		Transistor Array	CA3037A	ICAN-5213	310	Operational Amplifier
A3019	ICAN-5299	236	Diode Array	CA3038	ICAN-5290	316	Operational Amplifier
A3020	ICAN-5766	339	Multipurpose Wide-Band Amplifier	CA3038A	(,	310	Operational Amplifier
A3020A	.5/111 0/00	000	Multipurpose Wide-Band Amplifier	CA3039			Diode Array

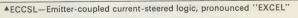
	<b>Electrical Characte</b>	ristics at T <sub>A</sub> =2	25°C						
	Static			Dynamic					
P	Typical Static Forward-Current Transfer Ratio h <sub>FE</sub>	Min. Collector -Substrate Breakdown Voltage	Max. Collector Cutoff Current Icso  µA	Typical hfe	Typical Gain Bandwidth Product f <sub>T</sub> MHz	Typical Open Circuit Output Cap. Ccbo pF	Typical Open Circuit Input Cap. Cebo pF	Device Package See Page 3	RCA Type
	1							Dio	de Arrays
	Typ. DC Forward Volta Min. DC Reverse Brea any Diode, 4V; any Di	kdown Voltage:	, 25V	any Diod	e, 10μA; any	kage Current: Diode & Subspacitance: 1.8p		TO-5	CA3019
	Typ. DC Forward Volt Min. DC Reverse Brea any Diode, 5V; any Di	kdown Voltage:	20V	any Diod	e, 0.1 μA; any	kage Current: Diode & Substance, 0.6p		TO-5	CA3039
								Transist	or Arrays
	100(Q <sub>1</sub> or Q <sub>2</sub> ) 5400(Ttl. Q <sub>3</sub> &Q <sub>4</sub> )	20	0.1	110 f=1kHz	500	0.58	0.6	TO-5	CA3018
	100 ( $Q_1$ or $Q_2$ ) 5400 (Ttl. $Q_3 \& Q_4$ )	40	0.04	110 f=1kHz	500	0.58	0.6	TO-5	CA3018A
	100 (Each Transistor)	20	0.1	110 f=1kHz		-Offset Voltage		TO-5	CA3026
	The following ratings transistor in the device Collector-to-Emitter V Collector-to-Base Volta Collector Current, I <sub>C</sub> .	e: oltage, V <sub>ceo</sub> tage, V <sub>ceo</sub> ge, V <sub>ceo</sub>	. 30 max. V		Power Gain	Darlington Pair: a = 47 dB at 1k a	Hz ms)	TO-5	CA3036
Po	100(Q <sub>1</sub> or Q <sub>2</sub> )	20	0.04	110 f=1kHz	550	0.58	0.6	DIC	CA3045
	100(Q <sub>1</sub> or Q <sub>2</sub> )	20	0.04	110 f=1kHz	550	0.58	0.6	DIP	CA3046
					10,000		FA	Amplif	ier Arrays
	D :: 101			Cracial Fu	notion Cub	nuotomo		7 (111)	CA3035
	Ratings and Chara				IIICLIOII Sub	systems			CA3035V1
	Ratings and Charac		DIP = Dual in-lir		ane				
	1CE-338 Mounting		Techniques to see for mounting	for RCA Integrand connection	grated Circuit	ts tegrated Circuit	ts and the		
	RCA Associated Application Note No.	Description			RCA A		File No. Description		
	CA3041 ICAN-5765 318 319 CA3042 331 CA3044 340 CA3044V1 CA3045 CA3046 Digital Integrated Circuit	Wide-Band Amp, FM Wide-Band Amp, FM High-Gain IF Amplif Detector, AM Prean High-Freq. Wide-Bal High-Freq. Wide-Bal Transistor Array Transistor Array	I Detector, AF Prea ier, Limiter, FM nplifier/Driver nd Amplifier/ Phase	mp/Driver  Detector  Detector	CD2203 CD2203D CD2204 CD2204D CD2205 CD2205D CD2300 Series CD2300D Series CD2300E Series	CAN-5271 2		Capability ne Package lal 4-Input NAND Ga ne Package lal 3-Input Gate (wit ne Package _ Circuits in ceramic _ Circuits in ceramic	h "wired" OR)
	CD2100 130 CD2101 300 CD2151 CD2152 CD2153 300 CD2200 CD2201 CD2201D CD2201D CD2201D CD2202D CD2202D CD2202D CD2202D CD2202D CD2202D CD2202D CD2202D 300	High-Speed ECCSL High-Speed ECCSL Ultra-High-Speed E OR/NOR Gate Ultra-High-Speed E OR/NOR Gate (with Ultra-High-Speed E OR/NOR Gate (with Ultra-High-Speed E Two-Input NOR Gate Low-Power DTL Du: Ceramic Dual In-Lir Low-Power DTL Qui Ceramic Dual In-Lir Low-Power DTL Qui Ceramic Dual In-Lir	Quadruple 2-Input CCSL Dual 4-Input CCSL Dual 4-Input "wired" OR) CCSL 8-Input "wired" OR) CCSL Quadruple e (with "wired" OR) al 4-Input NAND Ga le Package adruple 2-Input NAI e Package	OR Gate NOR Gate  te ND Gate  ffer Gate	Complementary TA5361 TA5362 TA5385	"N" and "P" Chan	Dual NOR Gate Plu Dual D-Type Flip-F Seven-Stage Ripple Dual Complementa 16-Bit Non-Destruc Quad 2-Input NOR Dual 4-Input NOR 0 18-Bit Shift Registe	is Inverter lop counter ry Pair Plus Inverte titive Readout Memo Gate Gate or	r ry



# 1

# Quick-Selection Chart

Descr	ription:	Medium-Po	wer DTL		Low-Power	r DTL	Ultra- High- Speed ECCSL▲	High- Speed ECCSL <sup>▲</sup>	COS/MOS*
	erating Temp.:	-55 to +12	25°C	0 to +75°C	-55 to +1	25°C	+10 to +60°C	-55 to +125°C	−55 to +125°C
Logic Pa	ackage:	14-lead ceramic flat pack	14-lead ceramic dual in-line	14-lead plastic dual in-line	14-lead ceramic flat pack	14-lead ceramic dual in-line	14-lead ceramic flat pack	14-lead ceramic flat pack	14-lead ceramic dual in-line
Gates									
Single 8-Input OR/NOR Complementary Output	ts	_		_	_		CD2152	_	_
Dual 3-Input Expandable AND/O Fransistor Output Pull- NOR and Inverter		г 	_	_	CD2205	CD2205D	_	_	_ TA5361 •
Dual 4-Input DR/NOR Complementary Ou Expandable NAND	tputs-	-	_	_		_	{CD2150, {CD2151•	CD2100	
Fransistor Output Pull- SkΩ Output Pull-up	up	_ CD2300/ 930	_ CD2300D/ 930	830		CD2200D —			Ξ
2kΩ Output Pull-up		CD2301/ 961	CD2301D/ 961	CD2301E/ 861		—		The state of the s	_
High Fanout Fransistor Output Pull-	-up	CD2306/ 932	CD2306D/ 932	CD2306E/ 832	CD2202	CD2202D	- Control of the Cont	_	Tanana .
'Wired OR'' Output Capability		CD2307/ 944	CD2307D/ 944	CD2307E/ 844		-	_		TA5456
NOR		_		_	_	_	_		1A5450
Triple 3-Input NAND 6kΩ Output Pull-up		CD2308/			_	_		Si con managana para para para para para para para p	A CONTRACTOR OF THE CONTRACTOR
2kΩOutput Pull-up		962 CD2309/ 963	962 CD2309D/ 963	862 CD2309E/ 863	_	-			
Quadruple 2-Inpu	ut								
Transistor Output Pull- 6kΩ Output Pull-up	-up	_ CD2302/	 CD2302D/ 946	 CD2302E/ 846	CD2201	CD2201D		_	
2kΩ Output Pull-up		946 CD2303/ 949		CD2303E/ 849	_				_
NOR		- 9	_	_	_		CD2153•	CD2101	TA5455



<sup>•&</sup>quot;WIRED-OR" output capability



<sup>\*</sup>Complementary-Symmetry MOS

Anticipated Commercial Announcement, August 1968.

Anticipated Commercial Announcement, late in 1968.

Description:	Medium-Po	ower DTL		Low-Powe	r DTL	Ultra- High- Speed ECCSL <sup>4</sup>	High- Speed ECCSL <sup>▲</sup>	COS/MOS*
Operating Temp.	—55 to +1	25 °C	0 to +75°C	-55 to +	125°C	+10 to +60°C	-55 to +125 °C	−55 to +125 °C
Logic Package Function	: 14-lead ceramic flat pack	14-lead ceramic dual in-line	14-lead plastic dual in-line	14-lead ceramic flat pack	14-lead ceramic dual in-line	14-lead ceramic flat pack	14-lead ceramic flat pack	14-lead ceramic dual in-line
Hex Inverters		*	-		*			
Diode Input 6kΩ Output Pull-up 2k ΩOutput Pull-up	CD2310/ 936 CD2311/ 937	CD2310D/ 936 CD2311D/ 937	CD2310E/ 836 CD2311E/ 837	New Actions	_		_	_
Expandable Input 6kΩ Output Pull-up 2kΩ Output Pull-up	CD2312 CD2313	CD2312D CD2313D	CD2312E CD2313E				_	
Gate Expanders					<del></del>			
Dual 4-Diode Input	CD2314/ 933	CD2314D/ 933	CD2314E/ 833	CD2204	CD2204D	_		
Flip-Flops								
J-K 2 Set, 2 Reset, 2J, 2K, Split Clock Inputs	_	_	_	CD2203	CD2203D	_	And a minimum an	_
Clocked R-S J-K Capability 6kΩ Output Pull-up	CD2304/ 945	CD2304D/ 945	CD2304E/		_	_		_
2kΩ Output Pull-up	CD2305/ 948	CD2305D/ 948				_	and contraction assumptions	_
Dual D	_	_	_			_		TA5362 ♦
MSI (Medium-Scale	ntegration	n)						
7-Stage Ripple Counter		,				_		TA5385 ♦ (TO-5 pkg.
16-Bit NDRO Memory Cell	_		_		_	_		TA5406 ♦
18-Bit Static Shift Register	_	_	_	Andreas and the Confederation of the Confederation		_		TA5459
General Purpose								
Dual Complementary Pair Plus Inverter	_		_	_		more and the second		TA5388 ♦

	Output	Voltage	Input C	Current	Output	Current		Max.	V-Adecisional					
7	"Low"	"High"	Re- verse	For- ward	Leak- age	Short Circ	cuit	Power Supply	Delay	agation ′			Device	1
	V	V	μΑ	mA	μΑ	mA		Current Drain	(t <sub>pd</sub> -	-)ns	(tpd-	⊢)ns	See Packag	RCA
	max	min	max	max	max	min	max	mA	min	max	min	max	Page 3	Type
								-				Dual	Four-I	nput Gate
	0.4	2.6	2	-1.6	50	-0.61	-1.34	6.5	10	30	25	80	CFP	CD2300/930
					New York			Management of the					CDL	CD2300D/93
	0.45	2.6	5	-1.4	100	-0.61	-1.3	8	10	30	25	80	PDL	CD2300E/83
	0.4	3.8	2	-1.6	50	-2.1	-3.7	10.9	10	30	15	50	CFP	CD2301/961
								•					CDL	CD2301D/96
	0.45	4.3	5	-1.4	100	-1.85	-3.68	11.8	10	30	15	50	PDL	CD2301E/86
	0.4	2.6	2	-1.6	50	-18		26.6	15	40	25	80	CFP	CD2306/932
					***	-							CDL	CD2306D/93
	0.45	2.6	5	-1.4	100	-16	_	30	15	40	25	80	PDL	CD2306E/83
	0.4	6b	2	-1.6	100	_	_	20	10	35	15	50	CFP	CD2307/944
													CDL	CD2307D/94
	0.45	6ь	5	-1.4	100	_	_	22.5	10	35	15	50	PDL	CD2307E/844
											1	Triple T	hree-I	nput Gates
	0.4	2.6	2	-1.6	50	-0.61	-1.34	9.75	10	30	25	80	CFP	CD2308/962
		VV000000000000000000000000000000000000								der for the first state and			CDL	CD2308D/962
	0.45	2.6	5	-1.4	100	-0.61	-1.3	12	10	30	25	80	PDL	CD2308E/862
	0.4	3.8	2	-1.6	50	-2.1	-3.7	16.35	10	30	15	50	CFP	CD2309/963
7			000000					mandage, 200-100-100-100-100-100-100-100-100-100-		more or v. st. should			CDL	CD2309D/963
10	0.45	4.3	5	-1.4	100	-1.85	-3.68	17.7	10	30	15	50	PDL	CD2309E/863
											Qua	druple	Two-I	nput Gates
	0.4	2.6	2	-1.6	50	-0.61	-1.34	13	10	30	25	80	CFP	CD2302/946
		recovered and the second								and an analysis of the second		V-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0-0	CDL	CD2302D/946
	0.45	2.6	5	-1.4	100	-0.61	-1.3	16	10	30	25	80	PDL	CD2302E/846
	0.4	3.8	2	-1.6	50	-2.1	-3.7	4.8	10	30	15	50	CFP	CD2303/949
					* "MANAGEMENT *			· · · · · · · · · · · · · · · · · · ·					CDL	CD2303D/949
	0.45	4.3	5	-1.4	100	-1.85	-3.68	23.6	10	30	15	50	PDL	CD2303E/849
													He	x Inverters
	0.4	2.6	2	-1.6	50	-0.61	-1.34	19.5	10	30	25	80	CFP	CD2310/936
								80 MIL. 000 A MIL. 000		december the anarrenges		STREET, STREET	CDL	CD2310D/936
	0.45	2.6	5	-1.4	100	-0.61	-1.3	24	10	30	25	80	PDL	CD2310E/836
	0.4	3.8	2	-1.6	50	-2.1	-3.7	32.7	10	30	15	50	CFP	CD2311/937
			-							ADDA ADA ADA ADA ADA ADA ADA ADA ADA AD				CD2311D/937
	0.5	4.3	5	-1.4	100	-1.85	-3.68	35.4	10	30	15	50	PDL	CD2311E/837
	0.4	2.6		-1.6	50	-0.61	-1.34	19.5	10	30	25	80	CFP	CD2312
										the second of			CDL	CD2312D
	0.45	2.6	-	-1.4	100	-0.61	-1.3	24	10	30	25	80	PDL	CD2312E
	0.4	3.8	-	-1.6	50	-2.1	-3.7	32.7	10	30	15	50	CFP	CD2313
			Santa Assault				7744	0.000		0000			CDL	CD2313D
7	0.5	4.3		-1.4	100	-1.85	-3.68	35.4	10	30	15	50	PDL	CD2313E
										Dual	Four	-Diode	e Input	Expander
	Input F	or. Volt.	per Dio	de		Current	er Diode		Curre	nt per Ex			CFP	CD2314/933
						•								
	0.7V m	in	0.82V	max		2μA max			10μΑ	max			CDL	CD2314D/933

Electrical	Characteristics	at	T,=	25°C
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	Output Voltage Input Curren		Current	Output	Max.	Vaccination								
7	"Low"	"High"	Re- verse	For- ward	Leak- age	Short Circ	cuit	Power Supply	Propa Delay	agation ′			Device	1
	V	V	μΑ		μΑ	mA		Current Drain	(tpd-	(t <sub>pd</sub> —)ns		-)ns	See	RCA
	max	min	max	max	max	min	max	mA	min	max	min	max	Page 3	Type
												Dual	Four-I	nput Gates
	0.4	2.6	2	-1.6	50	-0.61	-1.34	6.5	10	30	25	80	CFP	CD2300/930
								Adaptation					CDL	CD2300D/93
	0.45	2.6	5	-1.4	100	-0.61	-1.3	8	10	30	25	80	PDL	CD2300E/83
	0.4	3.8	2	-1.6	50	-2.1	-3.7	10.9	10	30	15	50	CFP	CD2301/961
								TOTAL ADMINISTRATION AND ADMINISTRATION ADMINISTRATION AND ADMINISTRATION ADMINISTRATION AND ADMINISTRATION AND ADMINISTRATION AND ADMINISTRATION AND ADMINISTRATION					CDL	CD2301D/96
	0.45	4.3	5	-1.4	100	-1.85	-3.68	11.8	10	30	15	50	PDL	CD2301E/86
	0.4	2.6	2	-1.6	50	-18	_	26.6	15	40	25	80	CFP	CD2306/932
							The state of the s						CDL	CD2306D/93
	0.45	2.6	5	-1.4	100	-16		30	15	40	25	80	PDL	CD2306E/83
	0.4	6b	2	-1.6	100		_	20	10	35	15	50	CFP	CD2307/944
													CDL	CD2307D/944
	0.45	6b	5	-1.4	100		_	22.5	10	35	15	50	PDL	CD2307E/844
											7	riple T	hree-li	nput Gates
	0.4	2.6	2	-1.6	50	-0.61	-1.34	9.75	10	30	25	80	CFP	CD2308/962
			_			0.01	1.01	0.70	10	00	20		CDL	CD2308D/962
	0.45	2.6	5	-1.4	100	-0.61	-1.3	12	10	30	25	80	PDL	CD2308E/862
	0.4	3.8	2	-1.6	50	-2.1	-3.7	16.35	10	30	15	50	CFP	CD2309/963
			889000000000000000000000000000000000000		Management of the Control of the Con	state of the state				The second secon			CDL	CD2309D/963
10-	0.45	4.3	5	-1.4	100	-1.85	-3.68	17.7	10	30	15	50	PDL	CD2309E/863
												<u> </u>		nput Gates
	0.4	2.6	2	-1.6	50	-0.61	-1.34	13	10	30	25		-	
	0.4	2.0	~	-1.0	30	-0.01	-1.34	13	10	30	25	80	CFP	CD2302/946
	0.45	2.6	5	-1.4	100	-0.61	-1.3	16	10	30	25	80	CDL	CD2302D/946
	0.4	3.8	2	-1.6	50	-0.01 -2.1	-1.3 -3.7	4.8	10	30	15	50	CFP	CD2302E/846 CD2303/949
	0.7	0.0	-	1.0	30	-2.1	-3.7	4.0	10	30	10	30	CDL	CD2303/949 CD2303D/949
	0.45	4.3	5	-1.4	100	-1.85	-3.68	23.6	10	30	15	50	PDL	
	0.10			1	100	1.00	-3.00	23.0	10	30	10	30		CD2303E/849
	0.4	0.0	0	4.0		0.04								x Inverters
	0.4	2.6	2	-1.6	50	-0.61	-1.34	19.5	10	30	25	80	CFP	CD2310/936
	0.45	0.0	-	4.4	400	0.04			10				CDL	CD2310D/936
	0.45	2.6	5	-1.4	100	-0.61	-1.3	24	10	30	25	80	PDL	CD2310E/836
	0.4	3.8	2	-1.6	50	-2.1	-3.7	32.7	10	30	15	50	CFP	CD2311/937
	0.5	4.0	_	4.4	4.00	4.05	0.00	05.4	10		4 =		CDL	CD2311D/937
	0.5	4.3	5	-1.4	100	-1.85	-3.68	35.4	10	30	15	50	PDL	CD2311E/837
	0.4	2.6		-1.6	50	-0.61	-1.34	19.5	10	30	25	80	CFP	CD2312
	0.45	2.6	-	4.4	4.00	0.04	4.0	0.4	10		0.5		CDL	CD2312D
	0.45	2.6		-1.4	100	-0.61	-1.3	24	10	30	25	80	PDL	CD2312E
	0.4	3.8		—1.6	50	-2.1	-3.7	32.7	10	30	15	50	CFP	CD2313
-	0.5	4.2		4.4	400	4.05	0.00	05.4	10		4.5		CDL	CD2313D
70-	0.5	4.3	_	-1.4	100	-1.85	-3.68	35.4	10	30	15	50	PDL	CD2313E
										Dual	Four	-Diode	e Input	Expander
		or. Volt.				Current p		The state of the s	Curre	ent per E	kpande	r	CFP	CD2314/933
	0.7V m		0.82V			2μA max			10μΑ				CDL	CD2314D/933
	0.68V r	min	0.84V	max		5μA max		state of the state	25μΑ	max			PDL	CD2314E/833



# Absolute Maximum Ratings at T<sub>A</sub> = 25 °C

Mediu	m-Power DTL (Con	tinued)	Power Input Current Cur				Amb Tem		re Range		
RCA Type	Description For Circuits, See Page 26	Features	Supply Voltage V	For- ward mA	Re- verse mA	(into output) mA	ating a		Stor- age °C		
	R-S Flip-Flops			1							
CD2304/94 CD2304D/	45 J-K Capability	Average power dissipation (per gate) :8mW typical     Average gate propagation delay     Continued	8 12ª	-10	1	30	-5! +1		−65 to +150		
CD2304E/8	345	:25ns typical (fan-out = 7+35pF)  • Gate fan-out capability— "pull-up" resistor = $6k\Omega \dots 8$					0 to +75		-25 to +85		
CD2305/94	18 J-K Capability	= 2kΩ7 • High Fan-out Gate drive	8	-10	1	30	-5	ō to	_65 to		
CD2305D/	with	• Noise immunity :1V typical at 25°C	12a	And Andrews and An			+1:	25	+150		
CD2305E/8	348	"Wired-OR" output logic capability     Single 4.5V to 5.5V power supply						+75	-25 to +85		
			a Pulsed;	duration le	ss than on	e second					
-						Ma	ximu	m Ratir	ngs		
							oient ip. Ra	nge	DC Supply		
RCA	Description					Ope		Stor- age	Voltage Range		
Type	For Circuits, See Page 28	Features				°C		°C	V		
Low-Po	ower DTL										
CD2200 CD2200D	Dual Four-Input Expandable NAND Gate	<ul> <li>Very low dissipation: 2.3mV</li> <li>Military full-temperature rai</li> <li>High noise immunity: 1.2V</li> </ul>	nge: -55°	C to +12			55 to 25	-651 +150			
CD2201 CD2201D	Quadruple Two-Input NAND Gate	<ul> <li>Active pull-up outputs—aff high capacitive loads</li> </ul>	ord low im	pedance	for drivin	-	55 to 25	-651 +150			
CD2202	Dual Four-Input Expandable NAND Buffer Gate	<ul> <li>High breakdown voltage in</li> <li>Popular pin configuration: 1</li> <li>Monolithic silicon epitaxial</li> </ul>	14 = V <sub>cc</sub> , 7 constructi		55 to 25	-651 +150					
CD2202D		<ul> <li>Aluminum-to-aluminum ult</li> <li>CD2202: Buffer Gate. Capa</li> </ul>			5 + 250 <sub>1</sub>	oF					
CD2203 CD2203D	J-K Flip-Flop	<ul> <li>CD2203: Split Clock Lines.</li> <li>Dual "K"Clock Steering Inp</li> </ul>	Dual "J" ( uts.Dual D	uts.	55 to 25	-651 +150	to +3.8 to +6.3				
CD2204	Dual Four-Input Gate Expander	<ul> <li>3MHz Complementing Rate</li> <li>CD2204: Capable of expansional respects with the compatible in all respects with the com</li></ul>	plementing Rate apable of expanding fan-in to more than 20 • in all respects with inputs of CD2200-Series gates					-651 +150			
CD2204D		CD2205: Performs AND/O	R/NOT fur								
CD2205 CD2205D	Dual Three-Input Expandable AND/OR/NOT Gate	<ul> <li>Expandable "AND" Section</li> <li>"D" Suffix: "D" Series are el versions, but utilize a 14-lea</li> </ul>	JUCK . A	55 to 25	-651 + 150						
Ultra-H	igh-Speed ECCSL®										
CD2150	Dual Four-Input OR/NOR Gate	■ Exceptionally high speed—				+1 +6	0 to	-551 +150			
CD2151	Dual Four-Input OR/NOR Gate with "Wired-OR"	<ul> <li>High system noise immunity</li> <li>Integral reference voltage summers</li> <li>Constant power supply current</li> </ul>	upply	logic swi	ng		0 to	-55 +150	to -4.5 to		
CD2152	Eight-Input OR/NOR Gate with "Wired-OR"	<ul> <li>Drives 100 Ω terminated line</li> <li>Complementary OR/NOR of</li> </ul>	ed lines				0 to	-55 +150	) —5.5		
CD2153	Quadruple Two-Input NOR Gate with "Wired-OR"	"Wired-OR" output capabil	lity			+1 +6	0 to	-55 +150			
High-S	peed ECCSL*									317	
CD2100	Dual Four-Input OR/NOR Gate	<ul> <li>High speed—Non-saturated</li> <li>Military full-temperature ran</li> </ul>	nge: -55°0	C to +12			55 to 25	-65 +150	→5. <b>7</b> 2		
CD2101	Quadruple Two-Input NOR Gate	<ul> <li>High system noise immunity</li> <li>Integral reference voltage su</li> <li>Constant power supply curr</li> </ul>	apply	ogic swii	ig		55 to 125	-65 +150			

# Electrical Characteristics at T<sub>A</sub> = 25°C

	Output '	Output Voltage		Input Current Output Current			Max.					- Control of the Cont		
T	"Low"	"High"	Re- verse	For- ward	Leak- age	Short Cir	cuit	Power Supply Current	Propa Delay	gation			Device	**************************************
	V	V	μΑ	mA	μΑ	mA	nA C		(tpd-	)ns	(t <sub>pd</sub> +)ns		Package See	RCA
	max	min	max	max	max	min	max	mA	min	max	min	max	Page 3	- magnetic
											(	Clocke	d R-S	Flip-Flops
	0.4	2.6	2	-1.07	50	-0.61	-1.34	14	30	75	35	75	CFP	CD2304/945
			2	-2.4	The same of the sa								CDL	CD2304D/945
			10	-3.2										•
	0.45	2.6	5	-0.95	100	-0.61	-1.3	15	30	75	35	75	PDL	CD2304E/845
		MANAGE AND	5	-2.1			The state of the s						minera for deliver appear	, , , , ,
			20	-2.8			and part product						Personal	
	0.4	3.8	2	2 -1.07 5	50 —	-2.1	-3.7	16.2	30	0 75	30	65	CFP	CD2305/948
			2	-2.4	-								CDL	CD2305D/948
			10	-2.56			Фологовина	N Pagi (Managan						
	0.45	4.3	5	-0.95	100	-1.85	-3.68	17.5	30	75	30	65	PDL	CD2305E/848
			5	-2.1			THE PROPERTY AND ADDRESS OF THE PROPERTY A							0225002/010
			20	-2.24			is for the same	- I among a company			-		-	
				CFP1	4-lead cer	amic flat pacl	CDL—1	4-lead ceram	ic dual-i	n-line pack	age	PDL-14-I	ead plastic du	ual-in-line package

CII 14-leau	ceramic nat pa

Lieu	LiiGai	Clia	racter	ISTICS	at	1 <sub>A</sub> =25	C
_							

	Operating Conditions	Static								mic	***************************************			
				The second secon		Max.	Тур.	Тур.	Prop.	Delay Time	- Andrews			
	Maximum	V <sub>cc</sub>	V <sub>cc</sub>	VEE		ic Levels	DC Input	Device Dissi-	Noise Immu-	Aver-	Fan-Out Loads+	Appl.	Device Package	RCA
<b>7</b> _	Fan-Out Per Gate	V	٧	"0" V	"1" V	Current mA	pation mW	nity V	age ns	Cap. (N+pF)	Note No.	See Page 3	Type	
		,									Lo	w-Pov	ver DTL	
	6	+4	0	+0.1	+3.4	-0.33	2.3 per gate	1.2	55	6+60	ICAN 5271	types with	CD2200D	
	6	+4	0	+0.1	+3.4	-0.33	2.3 per gate	1.2	55	6+60	ICAN 5271	"D" suffix have	CD2201 CD2201D	
	25	+4	0	+0.1	+3.4	-0.33	12 per gate	1.2	48	25+250	ICAN 5271	14-lead dual in-line	CD2202 CD2202D	
	5	+4	0	+0.1	+3.4	-0.33	7 per pkg.	1.2	130	5+50	ICAN 5271	ceramic pkg.;	CD2203 CD2203D	
	For input expansion of CD2200, CD2202, and CD2205 gates			expanding nore than			low cap	acitive l ed gate,	oading 4pF ty	on p.	ICAN 5271	other types have	CD2204	
	and CD2205 gates	for	ward vo	Itage, 0.72	2V typ.		leakage	current,	5nA ty	/p.		14-lead flat	CD2204D	
	_	+4	0	+0.1	+3.4	-0.33	5 per pkg.	1.2	71	6+60	ICAN 5271	pkg.	CD2205 CD2205D	
										Ultra-F	ligh-S	Speed E	CCSL*	
	12 (unterminated) 6 (100- $\Omega$ termination)		<b>—</b> 5		-0.76 unloaded	0.186	220 per pkg.	0.35	3.6 7.3	1+10 6+60	ICAN 5025	14-lead flat	CD2150	
	12 (unterminated) 6 (100- $\Omega$ termination)	0	<b>-</b> 5		-0.76 unloaded	0.186	175 per pkg.	0.35	3.6 7.3	1+10 6+60	ICAN 5025	pkg.	CD2151	
	12 (unterminated) 6 (100- $\Omega$ termination)	0	-5	-1.6 outputs	-0.76 unloaded	0.186	147 per pkg.	0.35	3.6 7.3	1+10 6+60	ICAN 5025		CD2152	
	12 (unterminated) 6 (100- $\Omega$ termination)	0	<b>-</b> 5	-1.6 outputs	-0.76 unloaded	0.186	175 per pkg.	0.35	3.6 7.3	1+10 6+60	ICAN 5025		CD2153	
(								-			ligh-S	peed E	CCSL*	
	12 (unterminated) 6 (300- $\Omega$ termination)	0			-0.75 unloaded	0.1	88 per pkg.	0.32	6 24	1+10 6+60	ICAN 5025		CD2100	
	12 (unterminated) 6 (300- $\Omega$ termination)	0	-5.2	-1.55 outputs	-0.75 unloaded	0.1	120 per pkg.	0.32	6 24	1+10 6+60	ICAN 5025	pkg.	CD2101	

								5°C	$t T_A = 2$	stics a	naracteri	trical C	I Elect	Typica	
	Device		Propag Delay	itance	Input Capaci	Noise Immu- nity		Device Dissipa (quiesc		DC In	Levels	Logic		Operati Voltage	
RCA Type	Package See Page 3	t <sub>pd</sub> ''1'' ns	t <sub>pd</sub> "0" ns	In- verter pF	Per Gate pF	40% of V <sub>DD</sub> V	V <sub>IN</sub> = 10V nW	V <sub>IN</sub> = OV nW	V <sub>IN</sub> = 10V pA	V <sub>IN</sub> = OV pA	V "1" V	V "0" V	V <sub>ss</sub>	V <sub>DD</sub>	
Gates															
TA5361	14-lead dual in-line ceramic		65 at C <sub>L</sub> =3	1.5	2.5	4	10	10	10	10	9.99	0.01	0	+10	
TA5455	14-lead dual in-line ceramic		65 at C <sub>L</sub> =3	distribute of commissions and control matter the commissions	2.5	4	10	10	10	10	9.99	0.01	0	+10	
TA5456	14-lead dual in-line ceramic		65 at C <sub>L</sub> =3		2.5	4	10	10	10	10	9.99	0.01	0	+10	
p-Flop	Fli														
TA5362	14-lead dual in-line ceramic		90	Term. eset	8.5-R Term. 5-Clo	4	10	10	10	10	9.99	0.01	O more supplied to	+10	
					Clock-P			"1" Clo			sipation				
ration)	Scale Integ				Frequenc	า.)	ns (mir	n = 125	Juratioi	L	(typ.)	0.4mW	KHZ=	at 100	
TA5385	TO-5	150		ut Pulse eset	5-Inp 40-Re Input	4	And an annual control of the control	100	10	10	9.99	0.01	O .	+10	
	Dynamic Device Dissipation at 100kHz=0.4mW (typ.)  "0" and "1" Clock-Pulse Clock-Pulse Input Frequency = 4MHz (max.)														
TA5406	14-lead dual in-line ceramic	Sense = 15ns Pulse n = 50ns	Read Time Write	k, y, D, ense	20—> and S Line			100	10	10	Sense ent: =10µA =500µA	Curre	0	+10	
TA5459	14-lead dual in-line ceramic	200 =50pF Pulse (shift =2MHz	Clock Freq.		5-D L 50-CI Line	4		100	10	10	9.99	0.01	0	+10	
urpose	General P				_1									1	
					С	t T <sub>A</sub> = 25	tics at	aracteris	rical Ch	Electi	Typica				
					D	-	ource	Drain-So						DC	
	Device	out naci-			Drain-Sour Breakdown [V <sub>(BR) DSS</sub> ]	ления поставления поста		Leakage Current		reshold	Gate Th Voltage	ince	nducta	Transco (g <sub>fs</sub> )	
RCA Type	Device Package See Page 3	paci- ce	- Ca		Breakdown	annel	(I <sub>DSS</sub> )		44	"P"			"F		Ô

# Integrated-Circuit Sockets

The table below lists some commercially available sockets for integrated circuits by manufacturers' and/or suppliers' parts numbers. This list is based on manufacturers' and/or suppliers' published information in our files at the time this publication was printed, and is not necessarily complete. Sockets having mechanical and electrical characteristics comparable with those of the devices listed below may be available from other manufacturers and/or suppliers of electronic components.

For a discussion of various methods for mounting and connecting RCA Integrated Circuits in equipment, and the principal considerations involved in each method, refer to RCA Technical Bulletin ICE-338 "Mounting and Connection Techniques for RCA Integrated Circuits."

Integrated-Circuit Package Type	Mfr. or Supplier	Mfr's. or Sup- plier's Part No.	Description
14-Lead	AMP Inc.	583109-0 thru 7*	Header (requires crimping machine)
Flat Package		583110-0 thru 7*	Receptacle
	Azimuth Electronics	5100-2	For use at temperatures up to 200°C
	Barnes Corporation	MD-55	For use at temperatures up to 125°C
		MD-75	For use at temperatures up to 200°C
	Jettron Products, Inc.	71-062 71-005	Plug-in printed-circuit card
14-Lead Flat	Barnes Corporation	029-001	For production batch testing
Package in RCA Carrier		029-090	For laboratory or environmental application
14-Lead	Augat, Inc.	314-AG10	For printed-circuit boards
Dual In-Line	_	314-AG3A	For chassis mounting
(Plastic or		114-AG1B	High-temperature Teflon* for chassis mounting
Ceramic)		114-AG1A	High-temperature Teflon,
			for printed-circuit boards
	Barnes Corporation	029-275-01	For printed-circuit boards (maintains factory-lead taper)
		029-275-11	For chassis mounting (maintains factory-lead taper)
		029-271-01	Contactor for accepting device in 029-240 carrier for automatic testing
3-Lead TO-5 Style	Augat, Inc.	8058-1G19	Miniature type, Teflon, for chassis mounting
10 0 01,10		8058-39G3	Miniature type, Teflon, for printed-circuit boards
	Barnes Corporation	MG-802	Miniature, Teflon, press-fit type
		MGR-81	Miniature, Teflon, for printed-circuit boards
		MF-02-8	For chassis mounting, chamfered-lead entrance
		MF-03-8	For printed-circuit boards, chamfered-lead entrance
10-Lead	Augat, Inc.	8058-1G22	Miniature type, Teflon, for chassis mounting
TO-5 Style		8058-2HG1	Miniature type, Teflon,
			for printed-circuit boards
	Barnes Corporation	MG-1002	Miniature, Teflon, press-fit type
		MGR-102	Miniature, Teflon, for printed-circuit boards
		MF-02-10	For chassis mounting, chamfered-lead entrance
		MF-03-10	For printed-circuit boards, chamfered-lead entrance
	Sealectro Corp.	Series 60#	Press-fit type, Teflon
12-Lead	Barnes Corporation	MG-1201	Miniature, Teflon, press-fit type
TO-5 Style		MGR-121	Miniature, Teflon, for printed-circuit boards
		MF-02-12	For chassis mounting, chamfered-lead entrance
		MF-03-12	For printed-circuit boards, chamfered-lead entrance
	Sealectro Corp.	Series 60	Press-fit type, Teflon

# Registered Trade Mark, Sealectro Corporation \*Registered Trade Mark, E. I. DuPont De Nemours & Co. \* AMP Crimpac, Registered Trade Mark, AMP Incorporated

Manufacturers' and/or Suppliers' Addresses

AMP Inc., Harrisburg, Pennsylvania 17105

Augat Inc., 33 Perry Avenue, Attleboro,
Massachusetts 02703

Azimuth Electronics, Denville, N. J. 07834 Barnes Corporation, Lansdowne,

Pennsylvania 19050

Jettron Products, Inc., 56 Route 10, Hanover, N. J. 07936

Sealectro Corp., 225 Hoyt Street, Mamaroneck, N. Y. 10543



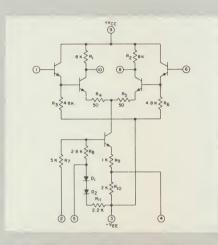
# **Differential Amplifiers**

# **DC** Amplifier

#### CA3000

- Schmitt Trigger
- Mixer
- Modulator
- Comparator
- Crystal Oscillator
- Sense Amplifier
- Wide AGC Range: 90dB typ.

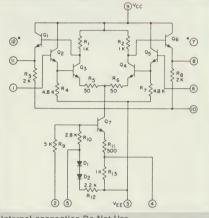
Package: 10-Lead TO-5



# Video and Wide-Band Amplifier

#### CA3001

- Schmitt Trigger
- Mixer
- Modulator
- DC, IF, Video Amplifier
- **Emitter Follower** Input & Output
- -3dB Bandwidth: 29MHz●



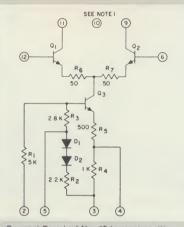
\*Internal connection Do Not Use.

# **RF/IF Amplifiers**

# CA3004

- Push-Pull Input & Output
- AGC
- Detector
- Mixer
- Limiter
- Wide & Narrow-Band Amplifier
- Cascode Amplifier
- Modulator

Package: 12-Lead TO-5



Note: Connect Terminal No. 10 to most positive dc supply voltage used for circuit.

# CA3005 **CA3006**

Package:

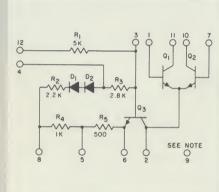
12-Lead TO-5

Package:

12-Lead TO-5

- High Gain, Sharp Limiting Characteristics
- Input Offset Voltage: CA3005 — 2.6mV typ. CA3006 — 1mV max.

(See CA3004 for Applications)



Note: Connect Terminal No. 9 to most positive dc supply voltage used for circuit.

CA3020 CA3020A Schematic diagrams for these types are shown under High-Gain Wide-Band Amplifiers

# **High-Gain Wide-Band Amplifiers**

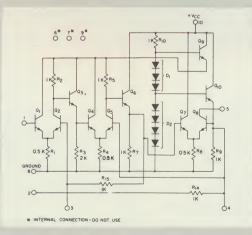
## FM/IF Amplifiers

# CA3011 CA3012

- High Amplifier Gain: 75dB at 4.5 MHz
- Limiting Sensitivity: 600µV at 10.7MHz (knee)
- Range: f=100kHz to >720MHz

CA3011 is like CA3012 except for Max. DC Supply Voltage: CA3011 — 7.5V CA3012 — 10V

Package: 10-Lead TO-5



# **Multipurpose Wide-Band Amplifiers**

# CA3020 **CA3020A**

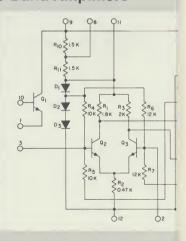
- In AF Applications combines functions of preamplifier, Phase-Inverter, Driver and Push-Pull Output
- Servo Amplifier

CA3020A is like CA3020

except for Power Output Class B Amplifier: CA3020 — 0.55W typ. CA3020A — 1W typ.

Sensitivity: CA3020 — 35mV typ. CA3020A - 50mV typ.

Package: 12-Lead TO-5

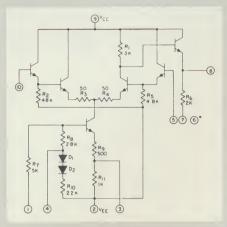


# IF Amplifier

# CA3002

- Product Detector
- IF & Video Amplifier
- AM Detector
- Schmitt Trigger
- —3dB Bandwidth: 11MHz

Package: 10-Lead TO-5



# **AF Amplifier**

#### CA3007

- Audio Driver and Amplifier
- Sound Systems and Communications Equipment
- Eliminates need for Audio Driver Transformer

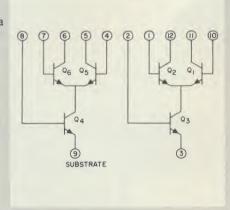
R<sub>12</sub> | 1K | R<sub>16</sub> | R<sub>16</sub> | R<sub>17</sub> | R<sub>18</sub> | R<sub>19</sub> | R

9 Q Vcc

#### CA3026

- Six Transistors on a Single Silicon Chip
- Connected as Two Differential Amplifiers with Constant Current Sinks

Package: 12-lead TO-5



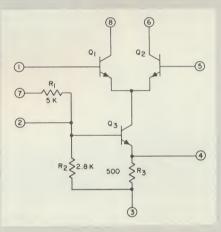
# CA3028A CA3028B

Package:

12-Lead TO-5

- Differential or Cascode Amplifier
- Limiter
- Mixer
- Converter, FM Broadcast
- Range: f=DC to 120MHz

CA3028B especially suitable for DC and Differential Amplifier Applications in Control Circuits Package:
8-Lead TO-5



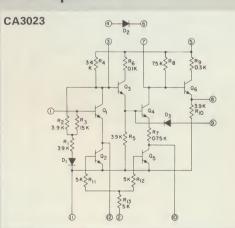
# **Video and Wide-Band Amplifiers**

# CA3021 CA3022 CA3023

- AM-FM IF Amplifier
- Limiter
- Wide AGC Range: 33dB typ.
- Power Drain: as low as 4mW
- Gain-Controlled Linear Types

Types differ in bandwidth capability, power drain, resistance values

Package: 12-Lead TO-5



CA3013 CA3014 CA3035 CA3035V1 CA3041 CA3042 CA3043

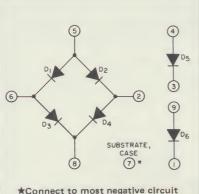
Schematic diagrams for these types are shown under Special-Function Subsystems

# Arrays

# Diode Arrays

### CA3019

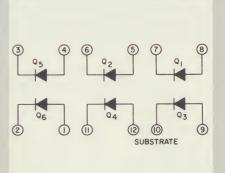
- Six Diodes on a Single Chip
- Matched Diodes
- Low Leakage
- Four in Bridge Configuration and Two Isolated Diodes



**★**Connect to most negative circuit potential

#### CA3039

■ Six Independent Diodes on a Single Chip



Package: 12-Lead TO-5

# **|Transistor Arrays**

# CA3018 CA3018A

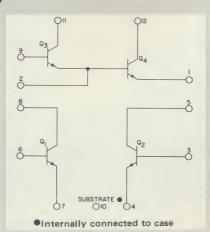
Package:

10-Lead TO-5

- Four General-Purpose Transistors, with Two in a Darlington Configuration
- Differential Amplifiers
- Low-Power Applications at f=DC through VHF A3018A is like CA3018

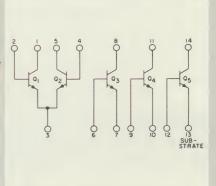
out features tighter controls for more critical applications requiring premium performance

Package: 12-Lead TO-5



# CA3045 CA3046

- Three Isolated Transistors and One Differentially-Connected Transistor Pair
- For all Types of Signal-Processing Systems operating from DC to VHF
- Temperature Compensated Amplifiers



Package: 14-Lead dual in-line, ceramic \* Package: 14-Lead dual in-line, plastic

#### ICA3036

- Two Independent Low-Noise Wide-**Band Amplifiers**
- AF Preamplifier and Low-Level Amplifier for Single-Channel and Stereo Systems
- **Matched Transistors** with Emitter-Follower Outputs
- f<sub>T</sub>=200MHz

7 6 Q2 3 2 1 (10)

Terminal 1, 5, 6, 7 internally connected to case

## CA3026

Schematic diagram for this type is shown under Differential Amplifiers

### CA3035 CA3035V1

Schematic diagrams for these types are shown under Special-Function Subsystems

Package: 10-Lead TO-5

Note: All resistance values are in OHMS

For Ratings and Characteristics on Linear Integrated Circuits, see pages 6-13 in the RCA Integrated Circuits Product Guide, CDL-820B

The resistance values included on the schematic diagrams have been supplied as a convenience to assist Equipment Manufacturer's in optimizing the selection of "outboard" components of equipment designs. The values shown may vary as much as  $\pm 30\%$ .

RCA reserves the right to make any changes in the Resistance values provided such changes do not adversely affect the published performance characteristics of the device.

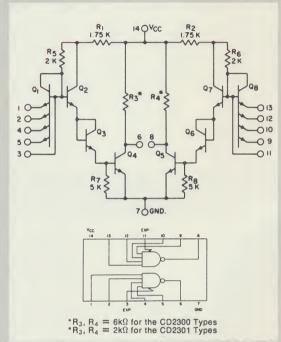


# **Medium-Power DTL**

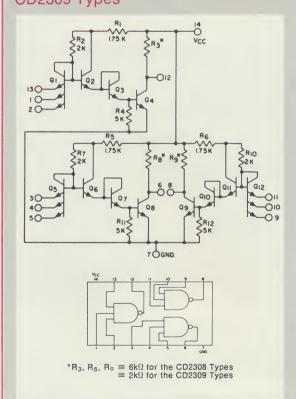
CD2300/930, CD2300D/930, CD2300/830 Series

**Dual 4-Input Gates** 

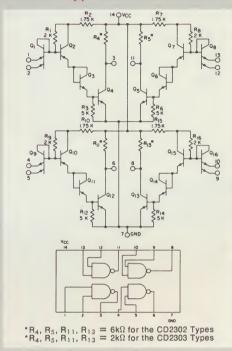
CD2300 Types CD2301 Types



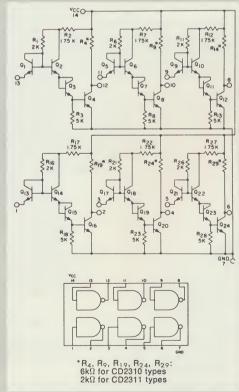
Triple 3-Input Gates CD2308 Types CD2309 Types



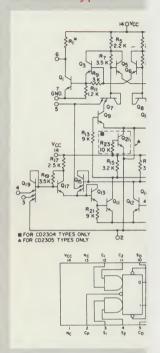
Quad 2-Input Gates CD2302 Types CD2303 Types



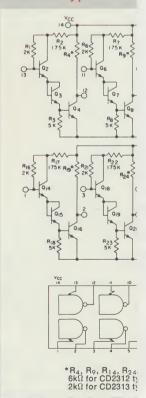
Hex Inverters with input diode CD2310 Types CD2311 Types



Clocked Flip-Flops CD2304 Types CD2305 Types



Hex Inverters expar CD2312 Types CD2313 Types



# **Features**

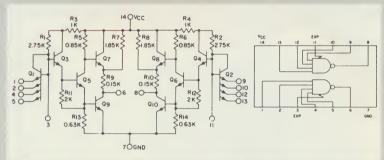
- · High Fan-out Gate drive capability, 25
- Noise immunity, 1V typical at 25°C
- "Wired OR" output logic capability
- Single 4.5V to 5.5V power supply
- Gate fan-out capability "pull-up" resistor  $= 6k\Omega \dots 8$

 $= 2k\Omega \dots 7$ 

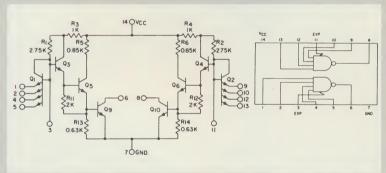
- Average power dissipation (per gate), 8 mW typical
- Average gate propagation delay, 25 ns typical (fan-out = 7 + 35 pF)
- Package: CD2300/930 Series -14-lead flat pack, ceramic CD2300D/930 Series -14-lead dual in-line, ceramic CD2300E/830 Series -

14-lead dual in-line, plastic

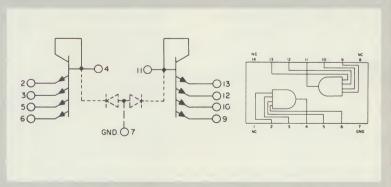
High Fan-Out Gates with transistor pull-ups CD2306 Types



High Fan-Out Gates — without pull-ups CD2307 Types



**Diode Expanders** CD2314 Types

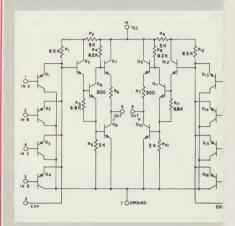




# Low-Power DTL

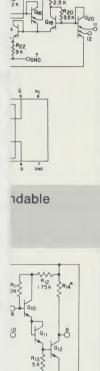
CD2200, CD2200D Series \*

**Dual 4-Input Gate** CD2200 CD2200D



#### Features

- Very low dissipation 2.3 mW/gate, 7 mW/ flip-flop
- Military full-temperature range: -55°C to +125°C
- High noise immunity
- · Active pull-up outputs afford low impedance for driving high capacitive loads
- High breakdown voltage input "Diodes"
- Popular pin configuration;  $14 = V_{cc}$ , 7 = Ground
- Monolithic silicon epitaxial construction
- Aluminum-to-aluminum ultrasonic bonding
- ★ Packages: CD2200 Series 14-lead flat pack, ceramic CD2200D Series 14-lead dual in-line, ceramic

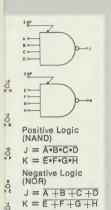


R29: /pes /pes

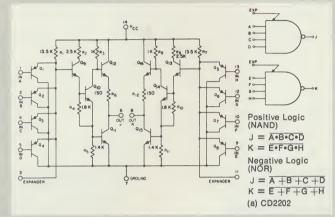
# **Dual 4-Input Buffer Gate**

CD2202, CD2202D

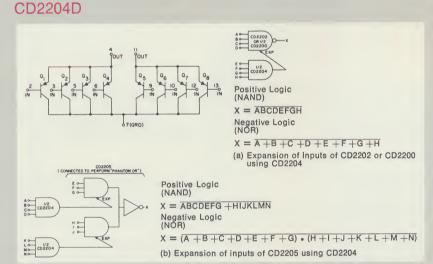
• Buffer Gate capable of Fan-Out of 25



(a) CD2200



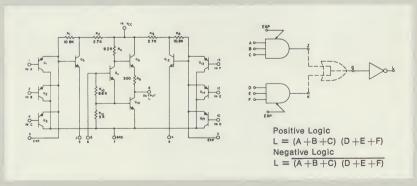
# Duai 4-Input Gate Expander CD2204



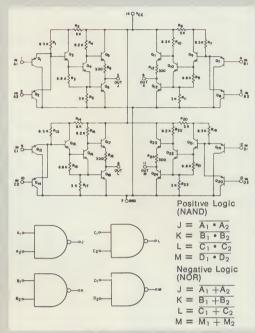
## **Dual 3-Input Gate**

## CD2205 CD2205D

- Performs AND/OR/NOT function
- Expandable "AND" sections "Wired OR" capability

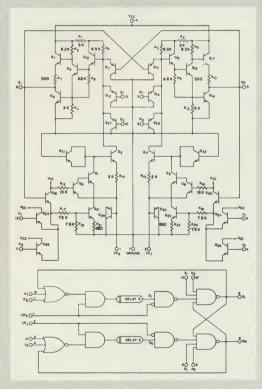


# Quadruple 2-Input Gate CD2201 CD2201D



# J-K Flip-Flop with set-reset capability CD2203, CD2203D

- Split Clock Lines
   Dual Inputs: "J" Clock Steering, "K" Clock Steering, DC Set & Reset
   1.2 MHz Complementing Rate



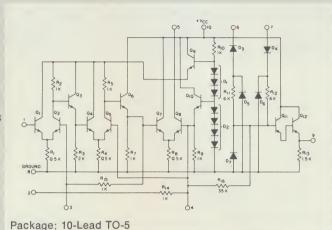


# **Special-Function Subsystems**

# Wide-Band Amplifier/Limiter/FM Detector

## CA3013 CA3014

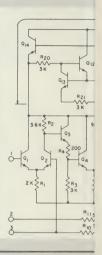
- Combines in one package
   IF Amplifier, AM and Noise Limiter, FM Detector, Audio Preamplifier
- AM Rejection: >50dB at 4.5MHz
- High Amplifier Gain: 75dB at 4.5MHz CA3013 is like CA3014 except for Max. DC Supply Voltage: CA3013=7.5V CA3014=10V



# Wide-Band Amplifier/Limiter/FN

#### CA3041

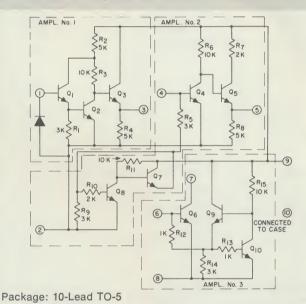
- For Sound-Systems of TV Receivers using Tube-Type AF Output Amplifiers
- For FM IF Amplifier Communications Systems and High-Fidelity Receivers to 20MHz
- Low Harmonic Distortion and Radiation
- Internal Regulated Voltage Supply
- Limiting Sensitivity: 150µV typ. at 4.5MHz



# **Ultra-High-Gain Wide-Band Amplifier Arrays**

# CA3035 CA3035V1

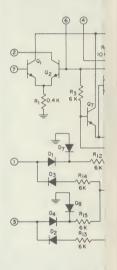
- Three Individual General-Purpose Amplifiers
- Ideal in Remote-Control Amplifiers,
- High Cascade Voltage Gain: 129dB typ. at 40kHz
- Low-Noise Wide-Band Performance
- Internal Temperature Compensation
- All Amplifiers Single Ended; requires one power supply
- CA3035V1 is like CA3035 but has formed leads



# **High-Frequency Wide-Band Amp**

## CA3034 CA3034V1

- AFC Applications
- Differential Input Amplifier
- Dual Phase Detector with Differential Output Amplifier
- Compensated Reference Voltage Supply
- CA3034V1 is like CA3034 but has formed leads

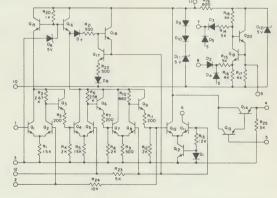


Package: 10-Lead T(

# High-Gain Wide-Band Amplifier/Limiter/FM Detector/Audio Amplifier

#### **CA3043**

- For FM IF Amplifier Communications Systems and High-Fidelity Receivers to 20MHz
- Limiting Sensitivity: 50μV typ. at 10.7MHz
- Low Harmonic Distortion
- Recovered AF Voltage: 100mV at 75kHz deviation



Package: 12-Lead TO-5

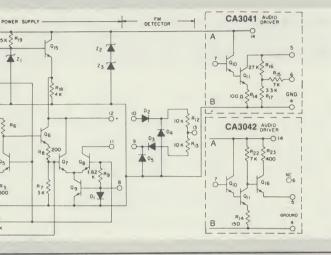
Note: All resistance va

For Ratings and Charasee pages 6-13 in the Guide, CDL-820B

The resistance value have been supplied Manufacturer's in opti ponents of equipment much as  $\pm 30\%$ .

RCA reserves the righ values provided such lished performance ch

# **Detector/Audio Driver**

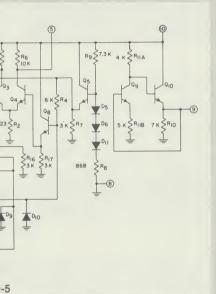


# CA3042

■ This type is like the CA3041 but is intended for sound systems of TV Receivers using Transistor-Type AF Output Amplifiers

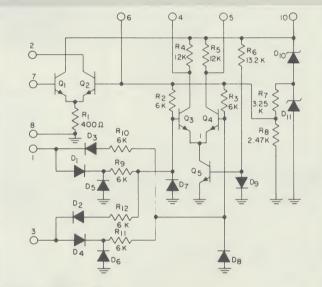
Package: 14-Lead dual in-line, plastic

# ifier/Phase Detectors



# CA 3044 CA 3044VI

- AFC Applications
- Internal Regulated Voltage Supply
- Differential Input Amplifier
- Dual Phase Detector with Differential Output Amplifier
- CA3044V1 is like CA3044 but has formed leads



Package: 10-Lead TO-5

lues are in OHMS

acteristics on Linear Integrated Circuits, RCA Integrated Circuits Product

s included on the schematic diagrams as a convenience to assist Equipment mizing the selection of "outboard" comdesigns. The values shown may vary as

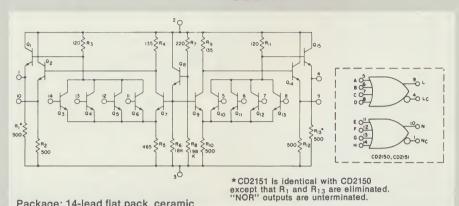
to make any changes in the Resistance changes do not adversely affect the pubaracteristics of the device.



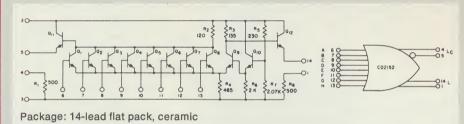
# **ECCSL\*-OR/NOR Positive Logic Ultra-High Speed**

**Dual 4-Input Gate** CD2150

**Dual 4-Input Gate** with "Wired OR" CD2151\*

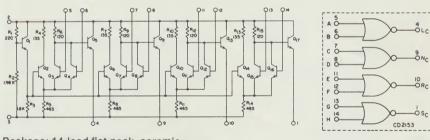


## 8-Input Gate CD2152



# Quadruple 2-Input Gate with "Wired OR" CD2153

Package: 14-lead flat pack, ceramic



Package: 14-lead flat pack, ceramic

#### CD2150, CD2151, CD2152, CD2153

Positive Logic Levei "1" = -0.76V "0" = -1.60V Equations L = A + B + C + D + E + F + G + H (OR)LC = A + B + C + D + E + F + G + H (NOR)

Negative Logic Level "1"= -1.60V Equations L = A.B.C.D.E.F.G.H (AND)  $LC = A \cdot B \cdot C \cdot D \cdot E \cdot F \cdot G \cdot H \text{ (NAND)}$ 

The resistance values included on the schematic diagrams have been supplied as a convenience to assist Equipment Manufacturer's in optimizing the selection of "outboard" components of equipment designs. The values shown may vary as much as  $\pm 30\%$ .

RCA reserves the right to make any changes in the Resistance values provided such changes do not adversely affect the published performance characteristics of the device.

For Ratings and Characteristics on Digital Integrated Circuits, see pages 16-21 in the RCA integrated Circuits Product Guide, CDL-820B.

Note: Ali resistance Values are in OHMS

## Ultra-High-Speed **Features**

- Exceptional High Speed Non-saturated operation, 3.6ns tpd, 1 + 10pF; 7.5ns tpd, 6 + 60 pF
- High system noise immunity, 40% of logic
- Integral reference voltage supply
- Constant power supply current
- Drives 100Ω terminated lines
- Complementary OR/ NOR outputs
- · "Wired OR" output capability
- Monolithic silicon epitaxial construction
- Aluminum-to-aluminum ultrasonic bonding
- Operating temperature range: +10 to +60°C

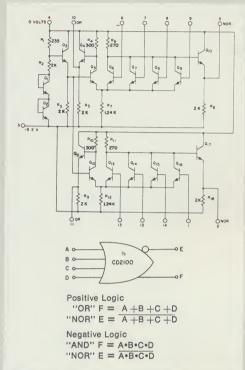
# High Speed **Features**

- High Speed Nonsaturated operation 6ns tpd, 1 + 10pF; 24 ns tpd, 6 + 60pF
- Military full-temperature range: -55°C to +125°C
- CD2100 -Complementary OR/NCR outputs
- Monolithic silicon epitaxial construction
- Aluminum-to-aluminum ultrasonic bonding
- High system noise immunity, 40% of logic swing
- Integral reference voltage supply
- Constant power supply current

▲Emitter-Coupled Current-Steered Logic, pronounced EXCEL

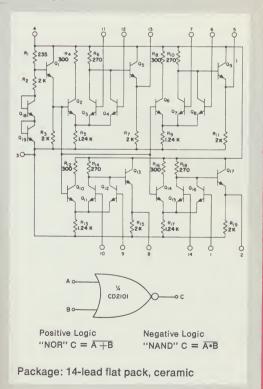
# **High Speed**

# Dual 4-Input OR/NOR Gate CD2100



Package: 14-lead flat pack, ceramic

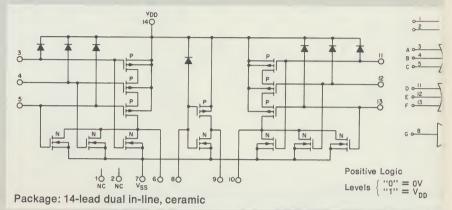
# Quadruple 2-Input NOR Gate CD2101



# COS/MOS (Complementary-Symmetry MOS)

# Dual 3-Input NOR Gate plus Inverter TA5361†

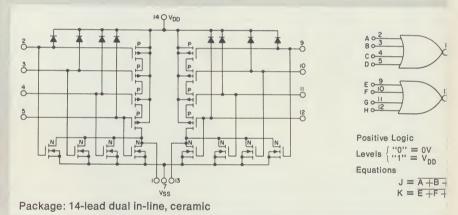
■ High-Speed Operation: t<sub>pd</sub> (av) = 50ns typ.



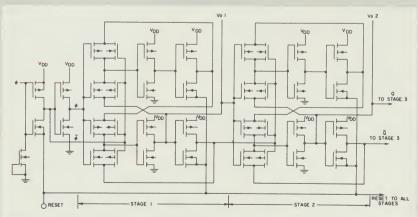
# **Dual 4-Input NOR Gate**

TA5456†

• High-Speed Operation:  $t_{pd}$  (av) = 50ns typ.



MSI (Medium-Scale Integration)
7-Stage Ripple Counter
TA5385†



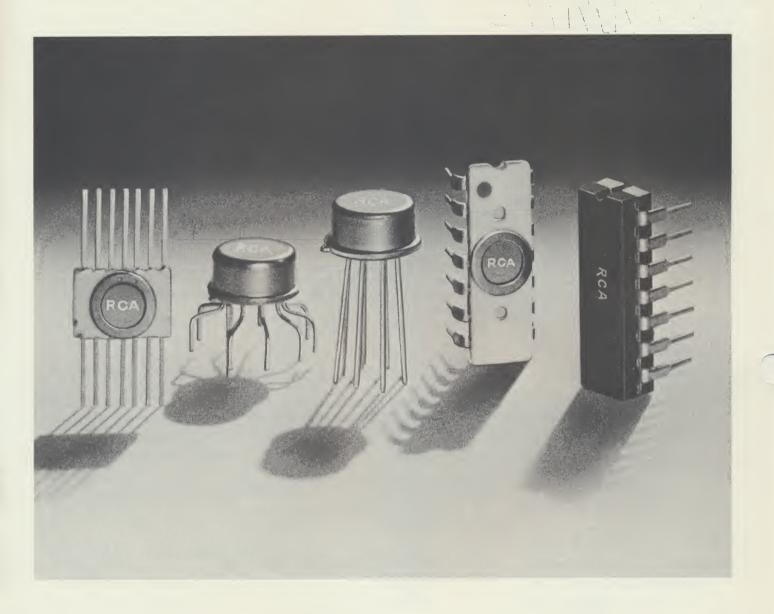
Package: 12-lead TO-5

†Developmental Type, Anticipated Commercial Announcement, August 1968

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